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PATENT

I hereby certify that on the date specified below, this correspondence is being deposited with the United States Postal Service as first-class mail in an envelope addressed to the Commissioner of Patents and Trademarks, Washington, DC 20231.

August 7, 2001
Date

L. Brooke Cornett

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Leland R. Nevill

Attorney Docket No.: 500060.01 (660073.555)

Patent No. : 5,984,190

Serial No. : 08/857,100

Issue Date : November 16, 1999

Filed : May 15, 1997

Title : METHOD AND APPARATUS FOR IDENTIFYING INTEGRATED CIRCUITS

REQUEST FOR CERTIFICATE OF CORRECTION

APPROVED

CERTIFICATE

AUG 16 2001

Commissioner of Patents and Trademarks
Washington, D.C. 20231

Sir:

JAN 24 2002
Bernard J. Dees
OR THE COMMISSIONER OF PAT & TM

OF CORRECTION

Pursuant to your response dated May 14, 2001, we are submitting a Request for Certificate of Correction. The references omitted in the patent were cited by the Examiner in Office Actions dated August 21, 1998, and February 25, 1999. Attached are copies of the Office Actions and the corresponding PTO-892 forms.

A Certificate of Correction under 35 U.S.C. § 254 is respectfully requested for the above-identified patent in order to correct Patent and Trademark Office errors made during the printing of the patent. The changes in the patent needed to correct the errors are as follows:

Please Add Additional U.S. Patent Documents to [56] References Cited

4,904,853	2/1990	Yokokawa	235/487
5,129,974	6/1992	Aurenius	283/81
5,360,747	11/1994	Larson et al.	437/8
5,641,164	6/1997	Doederlein et al.	273/237
5,742,526	4/1998	Voshell et al.	235/380
5,743,801	4/1998	Welander	463/43
5,748,731	5/1998	Shepherd	380/4
5,801,067	9/1998	Shaw et al.	438/15



The above errors for which correction is requested under 35 U.S.C. § 254 were made in the printing of the patent. The errors are considered sufficiently important to justify the processing of a Certificate of Correction under 35 U.S.C. § 254. A Form PTO-1050, in duplicate, is enclosed herewith.

Favorable consideration of this Request is respectfully requested.

Respectfully submitted,

Date: August 6, 2001

By:

A handwritten signature in black ink, appearing to read "Edward W. Bulchis".

Edward W. Bulchis, Reg. No. 26,847
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Enclosures:

Postcard

Form PTO-1050 (+ copy)

Copies of Office Actions dated Aug. 21, 1998 and Feb. 25, 1999

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UNITED STATES DEPARTMENT OF COMMERCE
Patent and Trademark Office
ASSISTANT SECRETARY OF COMMERCE AND
COMMISSIONER OF PATENTS AND TRADEMARKS
Washington, D.C. 20231

5-14-01

Patent No. : 5,984,190

Inventor(s) : Leland R. Nevill

Issued : November 16, 1999

For: METHOD AND APPARATUS FOR IDENTIFYING INTEGRATED CIRCUITS

Re: Request for Certificate of Correction

Consideration has been given your request for the issuance of a certificate of correction under Rule 1.322.

There is no Information Disclosure Statement in file with the references sought to be inserted via certificate of correction.

In view of the foregoing, your request is hereby denied.

L. Watson
Decisions & Certificate
of Correction Branch
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